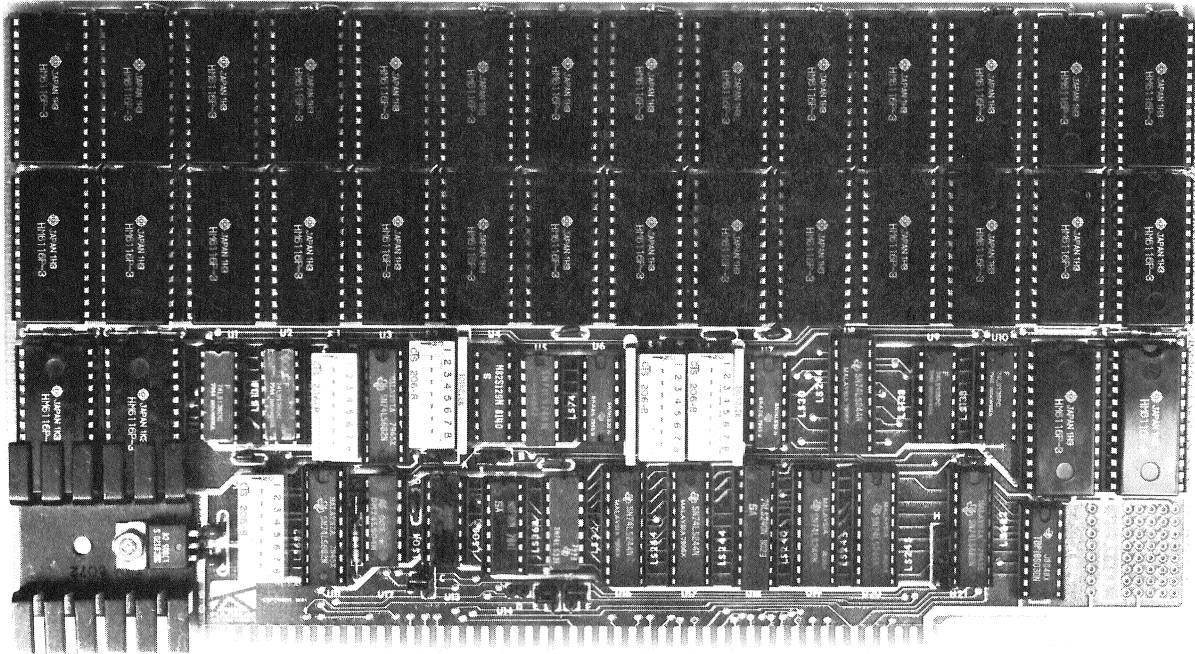


64K STATIC RAM BOARD FOR S-100 BUS

\$425

NEW: Additional Features



FEATURES

- Conforms to IEEE 696 standard.
- 8 or 16 bit data transfers.
- 24 bit addressing.
- Bank select in 32K-32K or 48-16K.
- Banks selectable/deselectable on DMA.
- Responds to phantom pin 67 or 16.
- 2Kx8 static rams with 2716 pin out.
- Power consumption is typically 600 ma.
- Banks on or off on power up.
- Bank addressable to any of 256 possible ports.
- 8MHz with 150ns parts standard faster speeds available on request.
- Available partially loaded as a 32K board.
- Multiple bank residence.

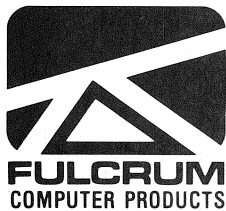
NEW FEATURES

- Phantoms read only, or read and write.
- Generates onboard M-WRITE, or uses bus M-WRITE.
- Two separate banks, independently addressable on 16K boundaries. Banks may be overlapped.

OMNIRAM REV. C

OMNIRAM INTRODUCTION:

The Fulcrum Computer Products OMNIRAM for the IEEE 696 (S100) bus provides 64 kilobytes of fast static random access memory. Provision is made for 8 or 16 bit transfers, extended 24 bit addressing, and for control via the bus phantom line. In addition, a number of features are included to make the OMNIRAM compatible with systems designed before the IEEE 696 standard was developed. These include bank selection and provision for operation with IMSAI-type front panels. When the bank select option is activated, the board is divided into two parts which can reside in separate banks. The division of the board may be into two 32K sections or into one 16K section and one 48K section. 2K blocks may be disabled in the upper 16K, or 4K blocks in the upper 32K, of memory. Provision is made for DMA override of bank select if needed. The board is also compatible with IEEE 696 or IMSAI-type extended addressing.



Omniram 64 United States & Canada Prices Only

	64K	32K
150ns. Hitachi Rams	\$425
100ns. Toshiba Rams	\$550
Board Without Ram	\$200

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