Symmetricom

TT&M Division

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CERTIFICATE OF VOLATILITY

Date: June 18, 2008

Model No. BC635/637PCI-V2

Model Description Time and Frequency Processor

This certification defines volatile and non-volatile memory devices for use by End Users in clearing User data for security purposes.

Memory Size	Type	User Data	IC Location
31-bytes	NVRAM	No	U2
Function:	U2 is the Real Time Clock that may be backed up by the on-board battery.		
Clearing Process:	U2 also contains 31-bytes of non-volatile RAM that is not used. The content of this RAM is lost when JP1 is removed. U2 is soldered to the PCB.		
----------	FPGA	No	U5
Function:	U5 is an FPGA that is reprogrammed each time the board is powered on. The configuration program for this part is contained in and programmed by U7.		
Clearing Process:	The content of this device is volatile and is lost when the board is powered down. U5 is soldered to the PCB.		
256KByte	FlashROM	No	U7
Function:	Microcontroller Flash memory used for program storage and for configuration data for FPGA U5. This part is not reprogrammed in normal operation.		
Clearing Process:	There is no clearing procedure for this memory. U7 is soldered to the PCB.		
12KByte	RAM	Yes	U7
Function:	Microcontroller RAM used as operating memory.		
Clearing Process:	The content of this memory is volatile and is lost when the bc635/637PCI-V2 is powered down.		
4KByte	EEPROM	No	U7
Function:	Microcontroller memory used to store non-volatile configuration data.		
Clearing Process:	There is no clearing procedure for this memory. U7 is soldered to the PCB.		

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2Kbit	**SEEPROM**	**No**	**U19**

Function: **Configures the PLX 9030 PCI host adapter.**

Clearing Process: **There is no clearing procedure for this memory. U19 is in a dual inline package that is mounted in an 8-pin socket.**

Neal Gors, **Quality Engineer, 6/18/2008**

Authorized Signature, Title, Date